



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/058,606	01/28/2002	Jinsaku Masuyama	016295.0748	3978

7590 11/24/2004  
Roger Fulghum  
Baker Botts L.L.P.  
One Shell Plaza  
910 Louisiana Street  
Houston, TX 77002-4995

EXAMINER

WILSON, YOLANDA L

ART UNIT PAPER NUMBER

2113

DATE MAILED: 11/24/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/058,606

Applicant(s)

MASUYAMA ET AL.

Examiner

Yolanda Wilson

Art Unit

2113

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 08/31/2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **SECOND DETAILED ACTION**

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Zaudtke et al. (US Publication Number 2003/0191877), further known as Zaudtke et al. As appears in claim 1, Zaudtke et al. discloses a plurality of independent sub-computer systems each comprising: a serial interface; and a buffer device coupled with the serial interface for buffering crash data sent by the serial interface having an external serial output; a management controller coupled with the external serial output of the buffer device of each independent sub-computer system to retrieve data buffered during a crash on page 5, paragraph 0041; pages 5-6, paragraphs 0046-0048. The management controller is the external device.

3. As per claim 2, Zaudtke et al. discloses a microcontroller having a memory and a serial input coupled with the serial interface and a serial output; a communication controller; a switch coupled with the serial output, wherein the switch is controlled by the communication controller on page 4, paragraph 0035; page 5, paragraph 0041,0044; page 3, paragraph 0032.

4. As per claim 3, Zaudtke et al. discloses wherein the serial output is part of a RS232 serial interface on page 4, paragraph 0035.
5. As per claim 4, Zaudtke et al. discloses the serial output is part of a universal bus serial interface on page 8, paragraph 0068.
6. As per claim 5, Zaudtke et al. discloses the communication controller is coupled with the management controller through a serial bus on page 5, paragraph 0044.
7. As per claim 6, Zaudtke et al. discloses the sub-system generates an interrupt signal fed to the management controller on pages 3 and 4, paragraph 0033.
8. As per claim 7, Zaudtke et al. discloses the sub-system generates an interrupt signal fed to the communication controller which generates an interrupt signal fed to the management controller and a control signal fed to the microcontroller on pages 5-6, paragraphs 0046-0048.
9. As per claim 8, Zaudtke et al. discloses a plurality of sub-systems each running independently an operating system and a management controller coupled with the plurality of sub-systems, the method comprising the steps of: upon a system crash outputting a crash dump file through a serial port of the sub-system; buffering the crash dump file; generating a control signal for a management controller; upon request by the management controller coupling the management controller with the sub-system; and transferring the buffered crash dump file to the management controller on page 5, paragraph 0041; pages 5-6, paragraphs 0046-0048. The management controller is the external device.

10. As per claim 9, Zaudtke et al. discloses the step of generating a control signal includes generating an interrupt signal fed to the management controller on pages 3 and 4, paragraph 0033.

11. As per claim 10, Zaudtke et al. discloses the step of generating a control signal includes sending a command to the management controller through a serial bus on page 5, paragraph 0044.

12. As per claim 11, Zaudtke et al. discloses wherein of coupling the management controller with the sub-system includes the step of coupling a serial output of the sub-system with the a serial communication line coupled with the management controller through a switch on page 3, paragraph 0033 and on page 4, paragraph 0047.

13. As per claim 12, Zaudtke et al. discloses wherein of coupling the management controller with the sub-system includes the step of coupling the management controller and the sub-system through a serial bus and sending a command through a serial bus to request transmission of the crash dump file on pages 5-6, paragraphs 0046-0048.

14. As per claim 13, Zaudtke et al. discloses a plurality of independent sub-systems each running a operating system that outputs a crash dump through a serial port and generates a control signal upon a system crash; a management controller having a control input, a serial bus interface coupled with a communication line, and a serial input; wherein each sub-system comprises: a microcontroller having a control input, a memory, and a serial input port coupled with the serial port and a serial output port; a controller unit having a serial bus interface for coupling with the management controller and an input for receiving the control signal and generating an external control signal

Art Unit: 2113

fed to the control input of the management controller and an output for an internal control signal fed to the microcontroller; a switch controlled by the controller unit for coupling the serial output port with the external communication line on page 5, paragraph 0041; pages 5-6, paragraphs 0046-0048. The management controller is the external device.

15. As per claim 14, Zaudtke et al. discloses the control signal and the external control signal are interrupt signals on pages 3 and 4, paragraph 0033.

16. As per claim 15, Zaudtke et al. discloses wherein the serial input and output ports are part of a RS232 serial interface on page 4, paragraph 0035.

17. As per claim 16, Zaudtke et al. discloses a plurality of independent sub-computer systems each comprising: a serial interface; and a buffer device coupled with the serial interface for buffering crash data sent by the serial interface having an external serial output; an interrupt signal output; a management controller coupled with the external serial output of the buffer device of each independent sub-computer system to retrieve data buffered during a crash on page 5, paragraph 0041; pages 5-6, paragraphs 0046-0048; on pages 3 and 4, paragraph 0033. The management controller is the external device.

18. As per claim 17, Zaudtke et al. discloses a microcontroller having a memory and a serial input coupled with the serial interface and a serial output; a communication controller; a switch coupled with the serial output, wherein the switch is controlled by the communication controller on page 4, paragraph 0035; page 5, paragraph 0041,0044; page 3, paragraph 0032.

Art Unit: 2113

19. As per claim 18, Zaudtke et al. discloses wherein the serial output is part of a RS232 serial interface on page 4, paragraph 0035.

20. As per claim 19, Zaudtke et al. discloses the serial output is part of a universal bus serial interface on page 8, paragraph 0068.

21. As per claim 20, Zaudtke et al. discloses the communication controller is coupled with the management controller through a serial bus on page 5, paragraph 0044.

### ***Response to Arguments***

22. Applicant's arguments with respect to claims 1-15 have been considered but are not persuasive. The arguments are not persuasive because of the new reference found, Zaudtke et al., to reject the added limitations in independent claims 1 and 8, the addition of claims 16-20, and the rejection of independent claim 13.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yolanda Wilson whose telephone number is (571) 272-3653. The examiner can normally be reached on M-F (7:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2113

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
ROBERT BEAUSOLIEL  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100